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Docket No. 031948-6 Serial No. 10/760,295 Page 1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re Patent Application or:)	,
Kenji ARAI et al.)	Group Art Unit: 2825
Serial No. 10/760,295)	Examiner: Yelena Rossoshek
Filed: 1/21/2004)	Confirmation No. 9845
For: METHOD OF PROTECTING A SEMICONDUCTOR INTEGRATED CIRCUIT FROM PLASMA DAMAGE)	Date: July 20, 2006

CERTIFICATE OF MAILING OR TRANSMISSION [37 CFR 1.8(a)]

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to: Mail Stop ______, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, or being the simile transmitted to the USPTO at 571 273 8300 on July 20, 2006.

AMENDMENT

Mail Stop

Commissioner for Patents
P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 20, 2006, please amend the above identified application as follows.

07/21/2006 TL0111 00000069 192380 10760295

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